RECEIPT #5

COPY OF PAPERS ORIGINALLY FILED



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

APPLICANT(S): Fwu-Iuan Hshieh et al.

SERIAL NO.: 10/010,484

FILING DATE: 11/20/01

TITLE: TRENCH MOSFET DEVICE WITH POLYCRYSTALLINE SILICON

SOURCE CONTACT STRUCTURE

ART UNIT: 2811

DOCKET: GS 150

Assistant Commissioner for Patents Office of Initial Patent Examination Customer Service Center Washington, DC 20231

LETTER REQUESTING CORRECTION TO FILING RECEIPT

This is in response to the filing receipt dated 01/10/2002.

Please make the following correction to the FILING RECEIPT:

Correct the filing date to November 20, 2001.

This correction is marked in red on the attached filing receipt copy.

Applicant's attorney attempted to file this application via Express Mail on November 20, 2001, however, on that date the United States Postal Service (USPS) refused to accept

the deposit of mail for delivery by Express Mail service. Per the instructions provided by the

U.S. PTO website at www.uspto.gov/september11/uspsmaildisrup.htm, the application was

re-deposited with the USPS on November 20, 2001 as first class mail, and was mailed to the

Assistant Commissioner for Patents at the special "Box Filing Date" address. Per the PTO's

instructions, a statement signed and dated by the person who originally attempted to deposit

the correspondence with the USPS as Express Mail was included, explaining the above

situation. A copy of said statement is enclosed for reference.

As the US PTO has designated this interruption in the Express Mail service of the

USPS as a postal service interruption within the meaning of 35 U.S.C. 21(a) and 37 CFR

1.6(e), and the special emergency procedures provided by the US PTO were correctly

followed, applicant's attorney submits that this application is entitled to the November 20,

2001 filing date.

It is respectfully requested that a corrected filing receipt be promptly issued that

accords this application a filing date of November 20, 2001.

Respectfully submitted,

Date:

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11/20/2001

PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS UNITED STATES PATENT AND TRADEMARK OFFICE WASHINGTON, D.C. 20231

www.uspto.gov

APPLICATION NUMBER FILING DATE GRP ART UNIT FIL FEE REC'D ATTY.DOCKET.NO DRAWINGS TOT CLAIMS IND CLAIMS 10/010.484 -12/10/2001 2811 758 GS 150 5 21

CONFIRMATION NO. 7850

FILING RECEIPT MAYER, FORTKORT & WILLIAMS, PC

OC000000007293574

2ND FLOOR WESTFIELD. NJ 07090

251 NORTH AVENUE WEST

Date Mailed: 01/10/2002

COPY OF PAPERS ORIGINALLY FILED

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Fwu-luan Hshieh, Saratoga, CA; Koon Chong So, Fremont, CA; John E. Amato, Tracy, CA; Yan Man Tsui, Union City, CA;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 01/10/2002

Projected Publication Date: 06/12/2003

Non-Publication Request: No

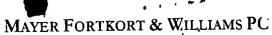
Early Publication Request: No

Title

Trench MOSFET device with polycrystalline silicon source contact structure

Preliminary Class

257



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November 20, 2001

Assistant Commissioner for Patents Box Filing Date Washington, DC 20231

Dear Sir:

Re: New U.S. Patent Application

Inventor: Fwu-Iuan Hshieh et al.

Filed: Herewith

Title: TRENCH MOSFET DEVICE WITH POLYCRYSTALLINE

SILICON SOURCE CONTACT STRUCTURE

Docket: GS 150

COPY



I hereby declare that I attempted to deposit the above-referenced new U.S. Patent Application with the United States Postal Service (USPS, Westfield, New Jersey) as Express Mail under 37 CFR 1.10 on November 20, 2001, and the USPS refused to accept this correspondence due to the recent emergency.

Per the instructions provided by the US PTO website (www.uspto.gov/september11/uspsmaildisrup.htm), the above-referenced application has been re-deposited with the USPS on November 20, 2001 as first class mail, in an envelope addressed to: Assistant Commissioner for Patents, BOX FILING DATE, Washington, DC 20231.

A November 20, 2001 filing date is respectfully requested.

Yours truly,

MAYER FORTKORT & WILLIAMS, PC

Marjorie Scariati Patent Paralegal

Enclosures